

Form PTO 1449 (Modified)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY DOCKET NO. 198707US-0X CONT	SERIAL NO. NEW APPLICATION
LIST OF REFERENCES CITED BY APPLICANT		APPLICANT	Johannes KRUL ET AL
		FILING DATE HEREWITH	GROUP UNKNOWN

931 U.S. Pro
99/692341

10/20/00

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
929	AA	4,218,674	08/19/80	Brosow et al	340	5,86	
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES	NO
929	AO	0 467 219 A2	✓ 07/11/91	Europe	X	
929	AP	WO 97 24699 A	✓ 07/10/97	Europe	X	
929	AQ	DE 196 01 358 A1	✓ 07/25/96	Germany		X
929	AR	EP 0 753 623 A2	✓ 01/15/97	Europe	X	
929	AS	WO 87/01845	✓ 03/26/87	Europe	X	
929	AT	GB 2 304 077 A	✓ 03/12/97	United Kingdom	X	
929	AU	WO 91/19614	✓ 12/26/91	Europe	X	
929	AV	WO 99/54842	✓ 10/28/99	Europe	X	

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

929	AW	Brown, A. R. et al: "Logic Gates Made From Polymer Transistors and Their Use in Ring Oscillators" Science, Vol. 270, 10 November 1995, pages 972-974, XP000644682
929	AX	Bonnoit, A. et al: "Evaluation Des Performances De Circuits Logiques Realises A Partir De Transistors Mis Organiques", Vol. 74, no. 4, 1 July 1994, pages 18-24, XP000438846.
	AY	
	AZ	

Examiner

Janet A. O'Donnell

Date Considered

5/13/2002

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.